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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: James H. Lie

Serial No.: 09/940,749

Title: METHOD AND/OR ARCHITECTURE FOR GENERATING SUPERSET PINOUT FOR DEVICES WITH COMBINED PROGRAMMABLE LOGIC AND HIGH-SPEED SERIAL CHANNELS

Filed: August 28, 2001

Attorney Docket No.: 0325.00503

Examiner: Thompson, A.

Art Unit: 2825

In Response To: Office Action mailed December 30, 2003

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 22, 2004.

By:

Jan M. Dunbar

AMENDMENT AFTER FINAL

Please Enter 6-May-2004
Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed December 30, 2003

please amend the above-identified application as follows: